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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/627,622	07/28/2003	Takashi Fujimura	240835US2	4330
22850	7590	10/05/2004	EXAMINER	
OBLON, SPIVAK, MCCLELLAND, MAIER & NEUSTADT, P.C. 1940 DUKE STREET ALEXANDRIA, VA 22314			ORTIZ, EDGARDO	
			ART UNIT	PAPER NUMBER
			2815	

DATE MAILED: 10/05/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

# Office Action Summary

Application No.

10/627,622

Applicant(s)

FUJIMURA, TAKASHI

Examiner

Edgardo Ortiz

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

## Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
  - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
  - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
  - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

## Status

- 1) ☒ Responsive to communication(s) filed on 13 September 2004.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

## Disposition of Claims

- 4) ☒ Claim(s) 1-24 is/are pending in the application.
- 4a) Of the above claim(s) 1-22 is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 23 and 24 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

## Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

## Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some \* c) ☐ None of:
- 1) ☒ Certified copies of the priority documents have been received.
  - 2) ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  - 3) ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

## Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
Paper No(s)/Mail Date \_\_\_\_\_
- 4) ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_\_
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: \_\_\_\_\_

## **DETAILED ACTION**

### ***Election/Restrictions***

1. Applicant's election with traverse of Group II, claims 23-24 received September 13, 2004 is acknowledged. The traversal is on the ground that search and examination of the whole application can be done without serious burden. This is not found persuasive because the invention of Group I, claims 1-22 is related to a method of manufacturing a thin-film transistor as classified in 438/+1, while the invention of Group II is a thin-film transistor as classified in 257/59. They are shown to be different inventions and having a separate status in the art by their different classifications. The requirement is still deemed proper and is therefore made FINAL.

### ***Claim Rejections - 35 USC § 102***

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 23-24 are rejected under 35 U.S.C. 102(b) as being anticipated by Yamaguchi et al. (U.S. patent No. 5,897,346). With regard to Claim 23, Yamaguchi discloses a thin film transistor (see column 1, lines 8-11) comprising:

an insulating (see column 15, lines 35-36) substrate (111);

a channel region (117) serving as a central portion of a semiconductor layer (112) having an island shape formed (see figure 11A) on the insulating substrate (111);

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a pair of source (116) and drain (118) regions formed at both sides (see figure 11B) of the channel region (117) in the semiconductor layer (112);

a desorption preventing layer (123) formed to cover (see figure 11C) at least the channel region (117) and the source (116) and drain (118) regions for preventing hydrogen terminating dangling bonds of the semiconductor layer (112) from desorbing from the dangling bonds (see column 16, lines 14-18 and column 19, lines 43-49); and

an interlayer dielectric film (119) formed (see figure 11D) on the desorption preventing layer (123).

With regard to Claim 24, Yamaguchi discloses (column 1, lines 17-20) a display device including a thin-film transistor (TFT) comprising:

an insulating (see column 15, lines 35-36) substrate (111);

a channel region (117) serving as a central portion of a semiconductor layer (112) having an island shape formed (see figure 11A) on the insulating substrate (111);

a pair of source (116) and drain (118) regions formed at both sides (see figure 11B) of the channel region (117) in the semiconductor layer (112);

a desorption preventing layer (123) formed to cover (see figure 11C) at least the channel region (117) and the source (116) and drain (118) regions for preventing hydrogen terminating dangling bonds of the semiconductor layer (112) from desorbing from the dangling bonds (see column 16, lines 14-18 and column 19, lines 43-49); and

an interlayer dielectric film (119) formed (see figure 11D) on the desorption preventing layer (123).

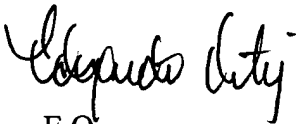
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***Conclusion***

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Edgardo Ortiz whose telephone number is 571-272-1735. The examiner can normally be reached on Monday-Friday (1st Friday Off).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tom Thomas can be reached on 571-272-1664. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



E.O.

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9/30/04